

DFO S1

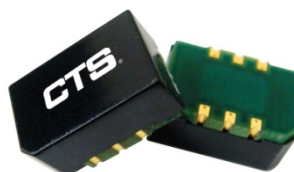
Miniature Surface Mount OCXO

9 x 14 mm, HCMOS



Features

- 10MHz to 50MHz frequency range
- HCMOS output
- Tight stability: ± 20 ppb



RoHS Status

Directive
2002/95/EC

Applications

- IP Timing
- Base Stations
- Femtocell
- LTE
- Network Timing and Synchronization

Electrical Specifications

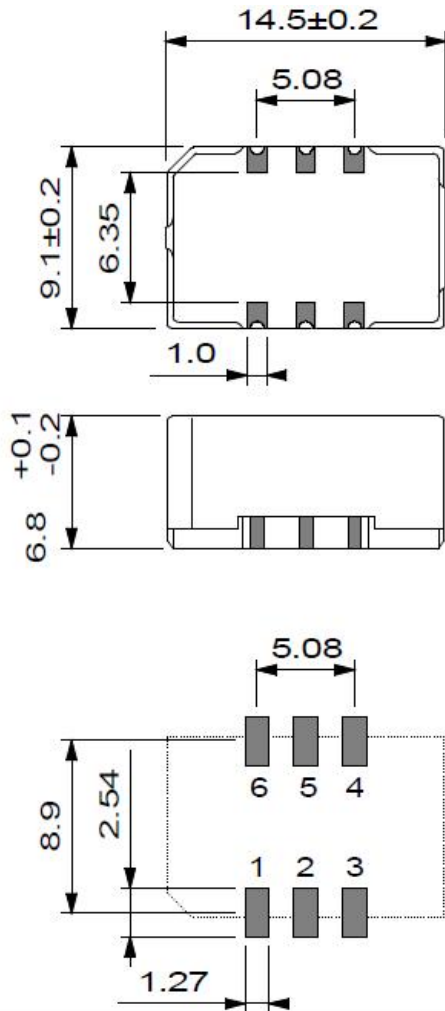
Parameter	Symbol	Condition	Min	Typ	Max	Unit	Note
Frequency Range	F		10		50	MHz	Note 1
Frequency Stability	$\Delta F/F$	vs. Operating Temperature -40°C to +85°C			± 50	ppb	See "How to Order" chart
		vs. Supply Voltage			± 20		
		vs. Load			± 10		
		vs. Aging			± 1	ppm	
		vs. Calibration @ +25°C			± 1		
Operating Temperature	T		-40		+85	°C	See "How to Order" chart
Supply Voltage			3.135	3.3	3.465	%	
Power Supply		Warm-up		1.4	1.6	W	
		Steady state, still air @ -40°C		0.6			
		Steady state, still air @ +25°C		0.3			
Output Load		HCMOS 15 pF or 2 TTL					
Duty Cycle		@ 50% level	45/55		55/45	%	
Rise/Fall Times		10% to 90%			5	ns	
High Levels			2.8			V	Output Amplitude
Low Levels					0.3		
Warm-Up Time		@ +25°C to reach ± 100 ppb			3	min	

Note 1: Standard Frequencies 10, 12.8, 13, 19.2, 19.44, 20, 25, 26 & 38.88 MHz

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Pin #	Connection
1	NC or V Control
2	NC or Tri-state (E / D)
3	GND
4	Output
5	NC
6	V _{CC}

How to Order

DFO S1 — L H Z 26 M C 28

Supply Voltage

Code	Specification
L	3.3V

Output Type

Code	Specification
H	HCMOS

Options

Code	Specification				
V	<table border="1"> <thead> <tr> <th>Specification</th> <th>Input Impedance</th> </tr> </thead> <tbody> <tr> <td> Voltage Control on Pin 1 $\geq \pm 5$ ppm, $\leq \pm 15$ ppm for 1.65V ± 1.35V; positive slope </td> <td>≥ 100 kΩ</td> </tr> </tbody> </table>	Specification	Input Impedance	Voltage Control on Pin 1 $\geq \pm 5$ ppm, $\leq \pm 15$ ppm for 1.65V ± 1.35 V; positive slope	≥ 100 k Ω
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Z	Tri-state				

Frequency

26 M

Temperature Range

Code	Specification
C	-20 to 70°C
E	-40 to 85°C

Stability

Code	Specification
58	± 50 ppb
28	± 20 ppb
17	± 100 ppb

