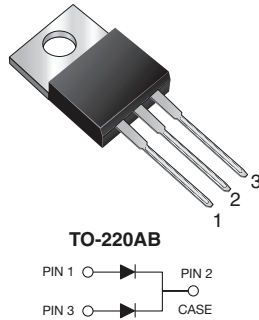


# Dual High Voltage TMBS® (Trench MOS Barrier Schottky) Rectifier

 Ultra Low  $V_F = 0.43 \text{ V}$  at  $I_F = 5 \text{ A}$ 


PRIMARY CHARACTERISTICS	
$I_{F(AV)}$	2 x 15 A
$V_{RRM}$	100 V
$I_{FSM}$	160 A
$V_F$ at $I_F = 15 \text{ A}$ (125 °C)	0.61 V
$T_J$ max.	150 °C
Package	TO-220AB
Circuit configuration	Common cathode

## FEATURES

- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- High efficiency operation
- Solder bath temperature 275 °C maximum, 10 s, per JESD 22-B106
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)


**RoHS**  
 COMPLIANT  
 HALOGEN  
**FREE**

## TYPICAL APPLICATIONS

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters, and reverse battery protection.

## MECHANICAL DATA

**Case:** TO-220AB

Molding compound meets UL 94 V-0 flammability rating  
 Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

**Polarity:** as marked

**Mounting Torque:** 10 in-lbs maximum

MAXIMUM RATINGS ( $T_A = 25 \text{ °C}$ unless otherwise noted)			
PARAMETER	SYMBOL	V31103C	UNIT
Maximum repetitive peak reverse voltage	$V_{RRM}$	100	V
Maximum average forward rectified current (fig. 1)	$I_{F(AV)}$	per device	30
		per diode	15
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode	$I_{FSM}$	160	A
Operating junction temperature range	$T_J^{(1)}$	-40 to +150	°C
Storage temperature range	$T_{STG}$	-55 to +150	

### Note

(1) The heat generated must be less than the thermal conductivity from junction to ambient:  $dP_D/dT_J < 1/R_{\theta JA}$



ELECTRICAL CHARACTERISTICS ( $T_J = 25\text{ }^\circ\text{C}$ unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	$I_F = 5\text{ A}$	$T_J = 25\text{ }^\circ\text{C}$	$V_F^{(1)}$	0.50	-	V
	$I_F = 7.5\text{ A}$			0.55	-	
	$I_F = 15\text{ A}$			0.68	0.73	
	$I_F = 5\text{ A}$	$T_J = 125\text{ }^\circ\text{C}$		0.43	-	
	$I_F = 7.5\text{ A}$			0.49	-	
	$I_F = 15\text{ A}$			0.61	0.66	
Reverse current at rated $V_R$ per diode	$V_R = 70\text{ V}$	$T_J = 25\text{ }^\circ\text{C}$	$I_R^{(2)}$	0.012	-	mA
		$T_J = 125\text{ }^\circ\text{C}$		6.5	-	
	$V_R = 100\text{ V}$	$T_J = 25\text{ }^\circ\text{C}$		-	0.9	
		$T_J = 125\text{ }^\circ\text{C}$		16.0	35	
Typical junction capacitance	4 V, 1MHz		$C_J$	1600	-	pF

**Notes**

- (1) Pulse test: 300  $\mu\text{s}$  pulse width, 1 % duty cycle  
(2) Pulse test: Pulse width  $\leq 5\text{ ms}$

THERMAL CHARACTERISTICS ( $T_A = 25\text{ }^\circ\text{C}$ unless otherwise noted)			
PARAMETER	SYMBOL	V31103C	UNIT
Typical thermal resistance per device	$R_{\theta JC}^{(1)}$	1.0	$^\circ\text{C/W}$

**Note**

- (1) Thermal resistance junction-to-case to follow JEDEC<sup>®</sup> 51-14 transient dual interface test method (TDIM)

ORDERING INFORMATION (Example)				
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
V31103C-M3/P	1.88	P	50/tube	Tube

**RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25\text{ }^\circ\text{C}$  unless otherwise noted)

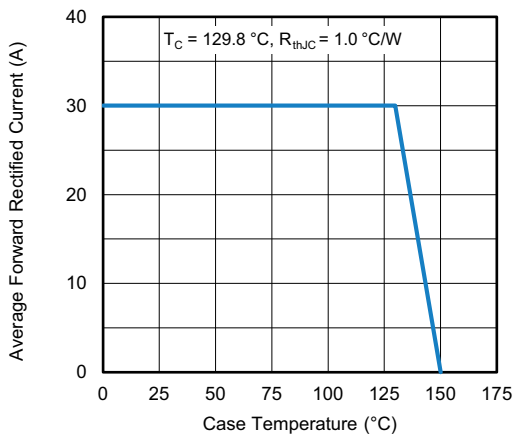


Fig. 1 - Forward Current Derating Curve

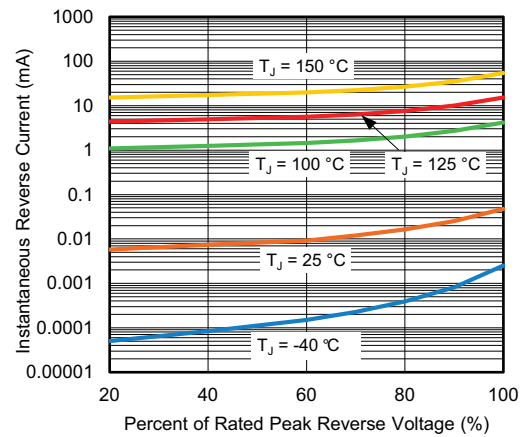


Fig. 4 - Typical Reverse Characteristics Per Diode

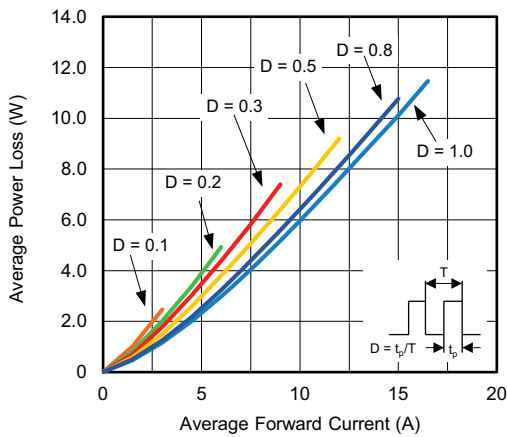


Fig. 2 - Forward Power Loss Characteristics Per Diode

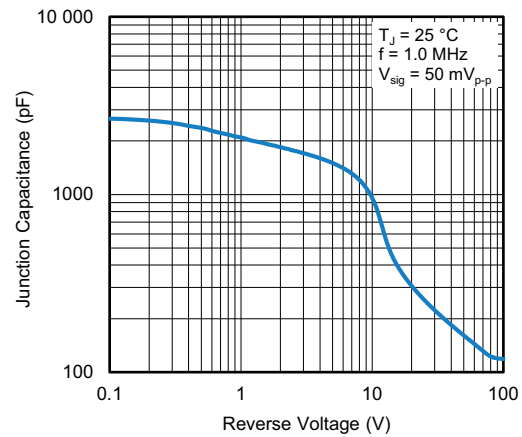


Fig. 5 - Typical Junction Capacitance

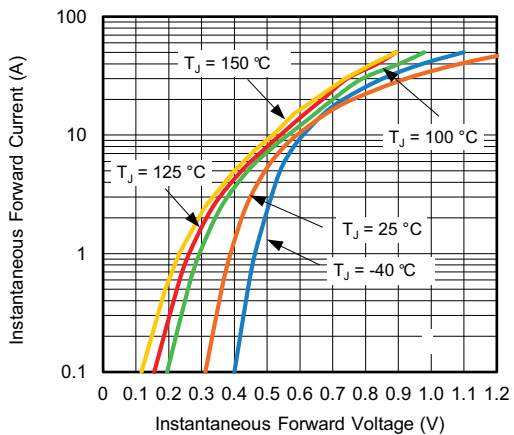


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

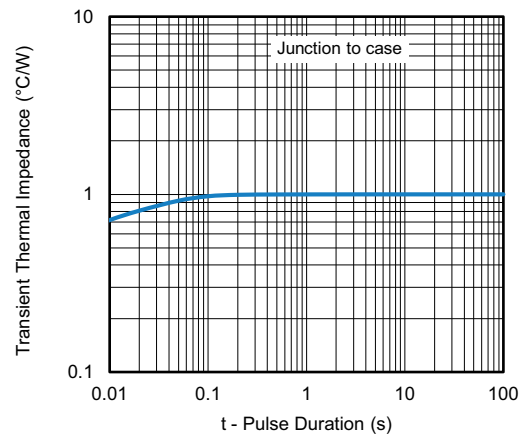
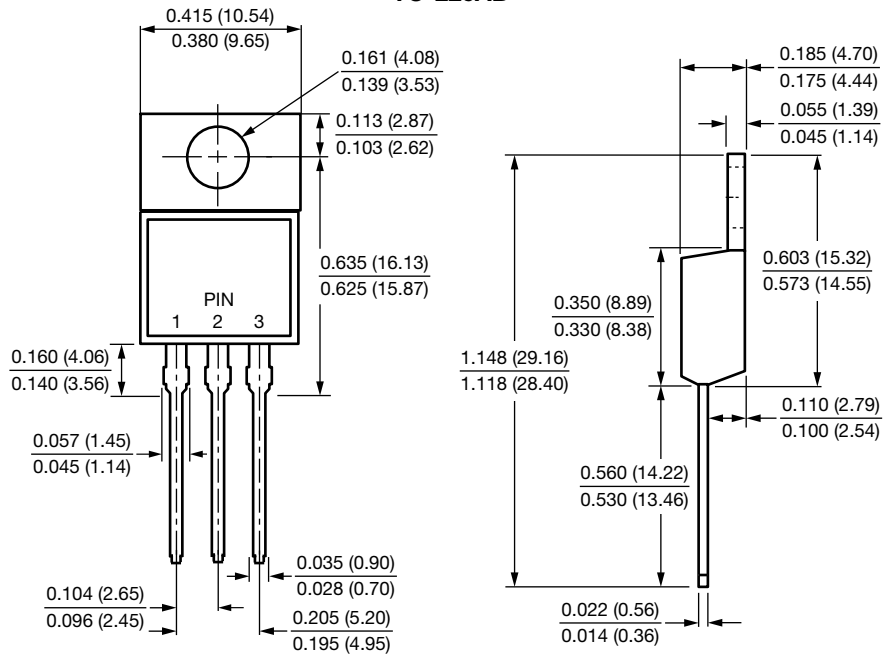


Fig. 6 - Typical Transient Thermal Impedance Per Device



PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

TO-220AB





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