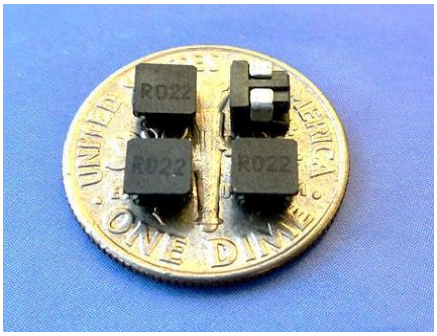
SLC1611 Series

1. Features of SLC1611 Series:

- Ferrite based SMD Inductor with lower core loss.
- Custom values are welcomed.
- High current output chokes, upto 41.00 Amp with approx. 20% roll off.
- Low Profile 3.00mm Max. height .
- Foot Print 4.00 x 4.00 mm Max.
- Ideal for Buck Converter, VRM & High Density Board Design.
- Operating frequency up to 1 MHz application.
- Operating Temperature Range -55°C to + 130°C. RoHs & HF compliant .
- T & R Qtys: 2400 pcs , 13" Reel.

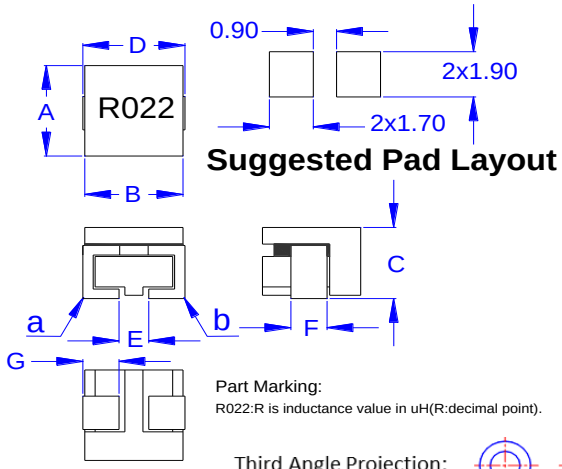


2. Electrical Characteristic of SLC1611 Series:

ITG Part Number	OCL [1] (nH) ± 20%	L@Isat1 [2] (nH) Min.	DCR [3] (mΩ) ± 9.0%	Isat1 [4] (A) @25℃	Isat2 [4] (A) @75℃	Isat3 [4] (A) @100℃	Isat4 [4] (A) @125℃	Irms [5] (A) @25℃
SLC1611A-R022MHF	22.00	15.00	0.23	41.00	38.00	36.00	33.00	22.00

3. Mechanical Dimension(Unit:mm):

A (Max.)	B (Max.)	C (Max.)	D (Max.)	E (Nom.)	F ±0.20	G ±0.20
4.00	3.95	3.00	4.00	1.10	1.40	1.40



Note:

1> Open Circuit Inductance (OCL) test condition:1.0MHz, 0.1Vrms, 0Adc at 25℃.

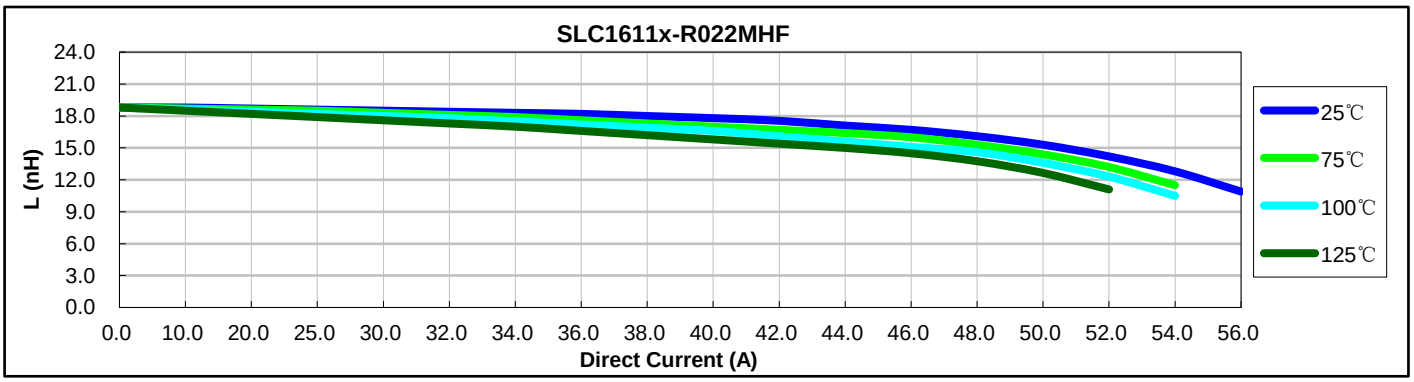
2> L @ Isat and L @ Irms Test condition:1.0MHz, 0.1Vrms (Ta=25℃).

3> The nominal DCR is measured from point "a" to point"b", as shown above on the mechanical drawing (Ta=25℃).

4> Isat1, Isat2, Isat3 & Isat4 : DC current that will cause inductance to drop approximately by 20%.

5> Irms: DC current for an approximate temperature rise of 40℃ without core loss. Derating is necessary for AC currents. PCB pad layout,trace thickness and width,air-flow and proximity of other heat generating components will affect the temperature rise. It is recommended the part temperature not exceed 130°C under worst case operating conditions as verified in the end application.

4. Inductance Characteristics (Inductance vs. Current):



● New York 1 914 347 2474 ● Taipei 886 2 2698 8669 ● Kaohsiung 886 7 350 2275
● Tokyo 81 3 5829 8676 ● Shenzhen 86 755 8418 6263 ● Shanghai 86 21 5424 5141 ● Hong Kong 852 9688 9767
● sales@inter-technical.com ● www.inter-technical.com

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