

#### **Features**

- Leading Trench Technology for Low R<sub>DS(on)</sub>
- · High Speed Switching
- Epoxy Meets UL 94 V-0 Flammability Rating
- · Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

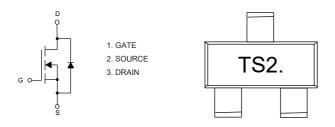
## **Maximum Ratings**

- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 625°C/W Junction to Ambient

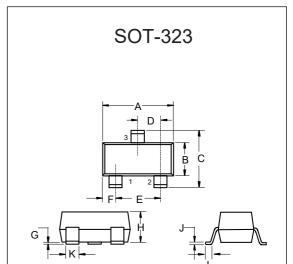
Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V <sub>DS</sub>	20	V
Gate-Source Volltage	V <sub>GS</sub>	±10	V
Continuous Drain Current	I <sub>D</sub>	3.0	Α
Pulsed Drain Current	I <sub>DM</sub>	14	Α
Total Power Dissipation	P <sub>D</sub>	0.2	W

Note: 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

# **Internal Structure and Marking Code**

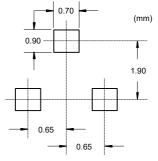


# N-CHANNEL MOSFET



DIMENSIONS						
DIM	INCHES		MM		NOTE	
DIIVI	MIN	MAX	MIN	MAX	NOIL	
Α	0.071	0.087	1.80	2.20		
В	0.045	0.053	1.15	1.35		
С	0.083	0.096	2.10	2.45		
D	0.026		0.65		TYP.	
Е	0.047	0.055	1.20	1.40		
F	0.012	0.016	0.30	0.40		
G	0.000	0.004	0.00	0.10		
Н	0.035	0.044	0.90	1.10		
J	0.002	0.010	0.05	0.25		
K	0.006	0.016	0.15	0.40		
L	0.010	0.018	0.26	0.46		

#### **Suggested Solder Pad Layout**





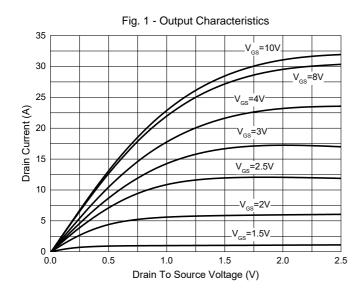
# Electrical Characteristics @ 25°C (Unless Otherwise Specified)

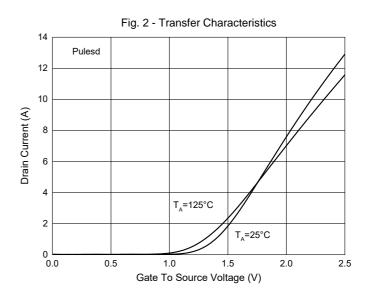
Parameter	Symbol		Test Conditions	Min	Ty	ур	Max	Un	nit	
Static Characteristics						<u>'</u>		1		
Drain-Source Breakdown Voltage	V <sub>(BR</sub>	)DSS	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	2	20				V	
Gate-Source Leakage Current	I <sub>GS</sub>	ss	V <sub>DS</sub> =0V, V <sub>GS</sub> =±10V				±1	00	nA	
Zero Gate Voltage Drain Current	I <sub>DS</sub>	ss	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V				1		μA	
Gate-Threshold Voltage	V <sub>GS</sub>	S(th)	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	0	.55	0.78	1		V	
Drain-Source On-Resistance <sup>(Note 2)</sup>	D		V <sub>GS</sub> =4.5V, I <sub>D</sub> =2.5A			57	7	-	m0	
	R <sub>DS</sub>	S(on)	V <sub>GS</sub> =2.5V, I <sub>D</sub> =2.0A			72	9		mΩ	
Diode Forward Voltage	Vs	SD	V <sub>GS</sub> =0V, I <sub>S</sub> =2.5A				1.	2	V	
Maximum Body-Diode Continuous Curre	nt I <sub>S</sub>						3.	0	Α	
Dynamic Characteristics <sup>(Note 3)</sup>										
Input Capacitance	Ci	ss				220				
Output Capacitance	Co	oss	V <sub>DS</sub> =10V,V <sub>GS</sub> =0V,f=1MHz			34			pF	
Reverse Transfer Capacitance	C <sub>r</sub>	ss				26				
Total Gate Charge	Q	l <sub>g</sub>				3.6				
Gate-Source Charge	Q	gs	V <sub>DS</sub> =10V,V <sub>GS</sub> =4.5V,I <sub>D</sub> =2.5A			0.88		nC		
Gate-Drain Charge	Q	gd				0.77				
Turn-On Delay Time	t <sub>d(c</sub>	on)				6.8				
Turn-On Rise Time	t,	r	$V_{DD}$ =10V, R <sub>I</sub> =1.5 $\Omega$ ,			57			no	
Turn-Off Delay Time	t <sub>d(c</sub>	off)	$R_{GEN}=3\Omega$			14			ns	
Turn-Off Fall Time	t,	f				53				

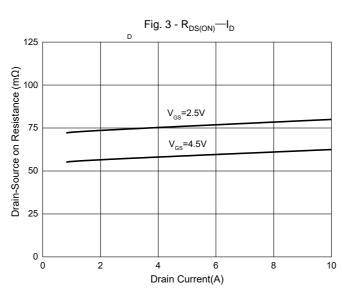
Note 2. Pulse Test : Pulse Width≤300µs, Duty Cycle ≤2%. 3. Guaranteed by Design, Not Subject to Production Testing.

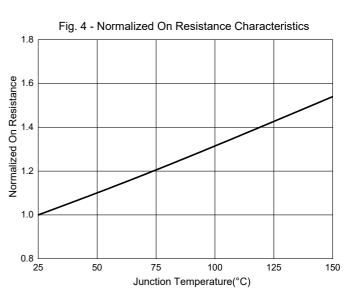


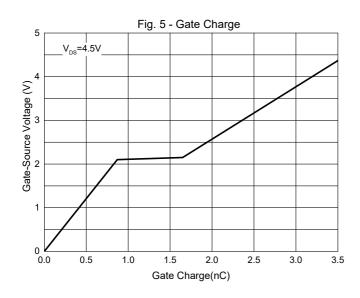
#### **Curve Characteristics**

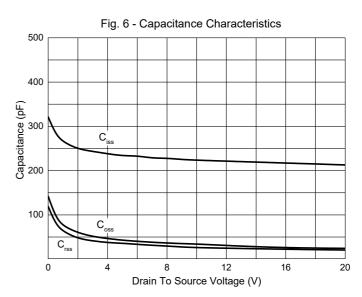














### **Ordering Information**

Device	Packing
Part Number-TP	Tape&Reel: 3Kpcs/Reel

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