

## Sealed Choke Coil SDEI054T type

### ■ Features

Low profile : 4.9mm x 4.9mm x 4.0mm

Low coil resistance with large currents.

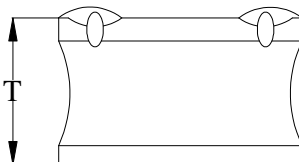
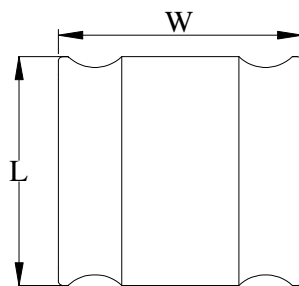
High magnetic shield construction should actualize high resolution for EMC protection.

100% lead (Pb) free meet RoHS standard

### ■ Application

Cellular phones, LCD displays, HDDs, DVCs, DSCs, PDAs etc..

### ■ Outline Dimensions



Code	Dimensions (mm)
L	4.9 ± 0.2
W	4.9 ± 0.2
T	4.0 max.

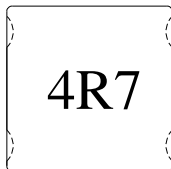
Note : This graph is in regard to outline dimensions spec. For outer appearance, please refer to actual product.

### ■ Marking

The inductor is marked with a 3-digit code

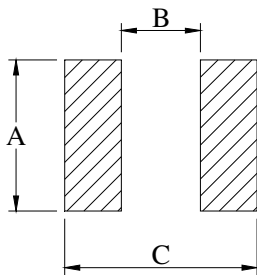
Example --  $4.7\mu\text{H}$  → 4R7

Upside of Chip



### ■ Recommend Land Pattern Dimensions

The customer shall determine the land dimensions shown below after confirming and safety.



A	4.0
B	2.1
C	5.1

Unit : mm

■ Specifications

Part Number	L0 Inductance ( $\mu\text{H}$ ) @ (0A)	$R_{dc}$ ( $\text{m}\Omega$ )		Heat Rating Current DC Amps. $I_{dc}$ ( A )		Saturation Current DC Amps. $I_{sat}$ ( A )	
		Typical	Maximum	Typical	Maximum	Typical	Maximum
SDEI054T-1R0MS	1.0	8.0	11.0	6.85	6.17	10.5	9.40
SDEI054T-1R5MS	1.5	13.0	16.0	5.50	4.90	9.00	8.0
SDEI054T-2R2MS	2.2	14.2	17.5	5.40	4.90	7.50	6.70
SDEI054T-3R3MS	3.3	20.0	25.0	4.60	4.14	6.40	5.76
SDEI054T-4R7MS	4.7	27.0	34.0	3.60	3.24	5.00	4.50
SDEI054T-6R8MS	6.8	41.0	50.0	3.15	2.84	4.55	4.10
SDEI054T-100MS	10.0	53.0	63.0	3.10	2.80	3.60	3.20
SDEI054T-120MS	12.0	69.0	83.0	2.70	2.43	3.30	3.00
SDEI054T-150MS	15.0	79.0	99.0	2.30	2.00	2.80	2.45
SDEI054T-220MS	22.0	119.0	149.0	2.10	1.90	2.40	2.10
SDEI054T-330MS	33.0	169.0	211.0	1.50	1.30	1.95	1.75
SDEI054T-470MS	47.0	235.0	294.0	1.35	1.20	1.60	1.44
SDEI054T-101MS	100.0	470.0	589.0	0.90	0.80	1.00	0.90

\* : If you require another part number please contact with us.

\*\* : Inductance Tolerance  $\pm 20\%$

Note 1. : All test data is referenced to 25°C ambient.

Note 2. : Test Condition:100KHz, 1.0Vrms

Note 3. :  $I_{dc}$  : DC current (A) that will cause an approximate  $\Delta T$  of 40°C

Note 4. :  $I_{sat}$  : DC current (A) that will cause L0 to drop approximately 30%

Note 5. : Operating Temperature Range -55°C to + 125°C

Note 6. : The part temperature (ambient + temp rise) should not exceed 125°C under the worst case operating conditions. Circuit design, component placement, PCB trace size and thickness, airflow and other cooling provision all affect the part temperature. Part temperature should be verified in the end application.

Note 7. : The rated current as listed is either the saturation current or the heating current depending on which value is lower.

### Current Characteristic

