Lead-free Green

600V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV_{DSS}	$R_{DS(ON)}$ Max	I_D T_A = +25°C
-600V	20Ω @ V_{GS} = -10V	-0.2A

Features and Benefits

- Low On-Resistance
- High BV_{DSS} Rating for Power Application
- Low Input Capacitance
- Fast Switching
- High Efficiency
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/**

Description and Applications

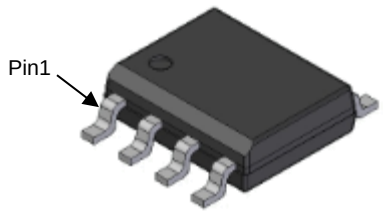
This MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- Motor Control
- Backlighting
- AC-DC Converters

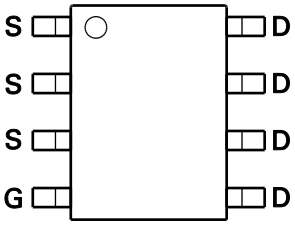
Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 3 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 e3
- Weight: 0.076 grams (Approximate)

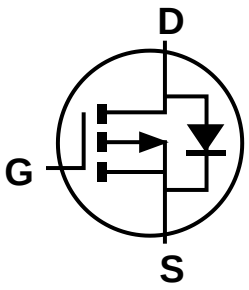
SO-8 (Standard B)



Top View



Pin-Out
Top View



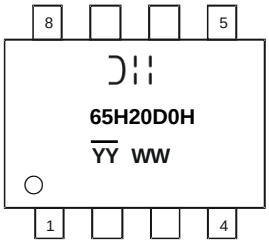
Equivalent Circuit

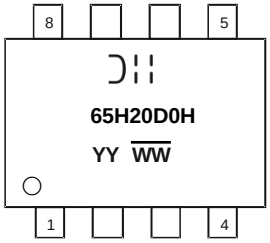
Ordering Information (Note 4)

Part Number	Case	Packaging
DMP65H20D0HSS-13	SO-8 (Standard B)	4,000 / Tape & Reel

Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information





= Manufacturer's Marking
65H20D0H = Product Type Marking Code
YYWW or YYWW = Date Code Marking
YY or YY = Year (ex: 21 = 2021)
WW or WW = Week (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage (Note 5)		V_{DSS}	-600	V
Gate-Source Voltage		V_{GSS}	±30	V
Continuous Drain Current (Note 6) V_{GS} = -10V	T_A = +25°C T_A = +70°C	I_D	-0.2 -0.16	A
Maximum Body Diode Forward Current (Note 6)	T_A = +25°C T_A = +70°C	I_S	-0.2 -0.16	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I_{DM}	-1.5	A
Pulsed Source Current (10µs Pulse, Duty Cycle = 1%)		I_{SM}	-1.5	A
Peak Diode Recovery dv/dt (Note 8)		dv/dt	5	V/ns

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation, @T_A = +25°C (Note 6)	P_D	1.9	W
Thermal Resistance, Junction to Ambient @T_A = +25°C (Note 6)	$R_{\theta JA}$	65	°C/W
Power Dissipation, @T_A = +25°C (Note 7)	P_D	1.25	W
Thermal Resistance, Junction to Ambient @T_A = +25°C (Note 7)	$R_{\theta JA}$	100	°C/W
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 9)						
Drain-Source Breakdown Voltage	BV_{DSS}	-650	—	—	V	V_{GS} = 0V, I_D = -250µA
Zero Gate Voltage Drain Current	I_{DSS}	—	—	-1	µA	V_{DS} = -650V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	100	nA	V_{GS} = ±30V, V_{DS} = 0V
ON CHARACTERISTICS (Note 9)						
Gate Threshold Voltage	$V_{GS(TH)}$	-2	-3	-4	V	V_{DS} = V_{GS}, I_D = -250µA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	15.4	20	Ω	V_{GS} = -10V, I_D = -0.2A
Diode Forward Voltage	V_{SD}	—	-0.7	-1.3	V	V_{GS} = 0V, I_S = -0.2A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	—	400	—	pF	V_{DS} = -25V, f = 1MHz, V_{GS} = 0V
Output Capacitance	C_{oss}	—	34	—		
Reverse Transfer Capacitance	C_{rss}	—	2.7	—		
Gate Resistance	R_g	—	13.7	—	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1MHz
Total Gate Charge	Q_g	—	9.7	—	nC	V_{DD} =-520V, I_D = -0.2A, V_{GS} = -10V
Gate-Source Charge	Q_{gs}	—	1.8	—		
Gate-Drain Charge	Q_{gd}	—	3.2	—		
Turn-On Delay Time	$t_{D(ON)}$	—	16	—	ns	V_{DD} = -325V, V_{GS} = -10V, R_G = 3Ω, I_D = -0.2A
Turn-On Rise Time	t_R	—	10	—		
Turn-Off Delay Time	$t_{D(OFF)}$	—	34	—		
Turn-Off Fall Time	t_F	—	76	—		
Body Diode Reverse Recovery Time	t_{RR}	—	154	—	ns	I_S = -1A, dI/dt = 100A/µs
Body Diode Reverse Recovery Charge	Q_{RR}	—	0.9	—	µC	

Notes:
5. HTRB V_{DS} maximum is -480V.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square pad layout.
7. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
8. Guaranteed by design. Not subject to production testing.
9. Short duration pulse test used to minimize self-heating effect.

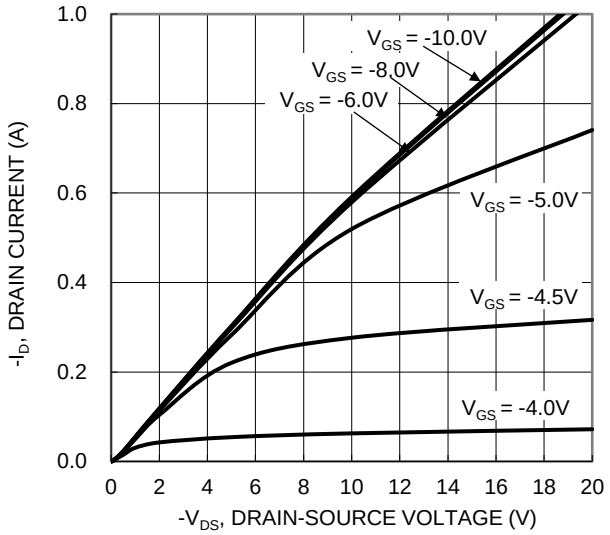


Figure 1. Typical Output Characteristic

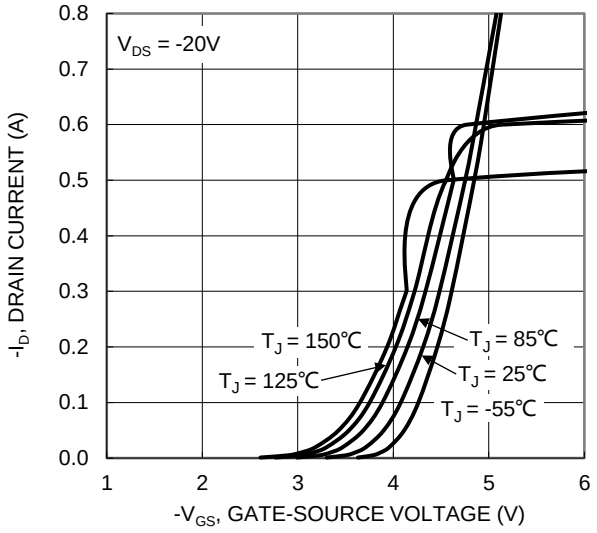


Figure 2. Typical Transfer Characteristic

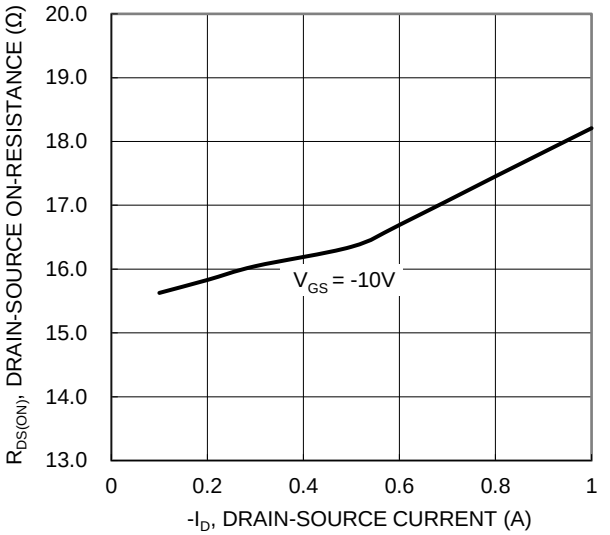


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

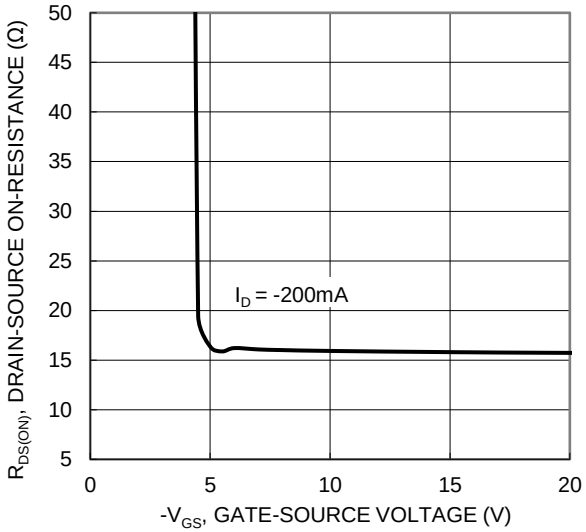


Figure 4. Typical Transfer Characteristic

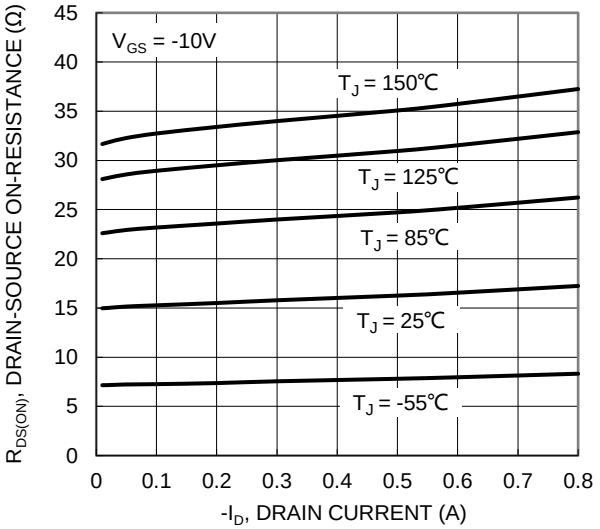


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

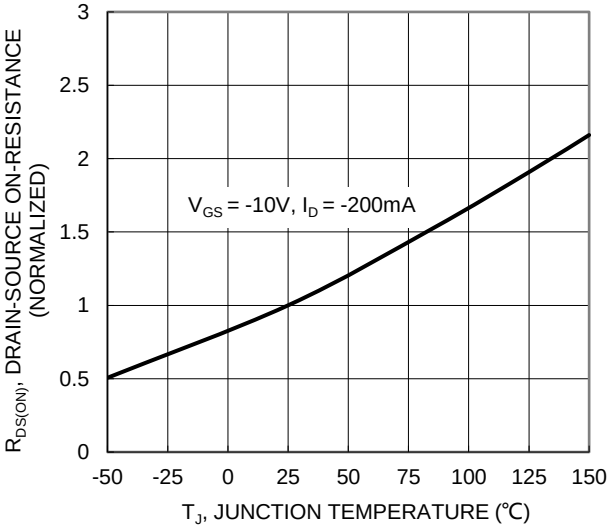


Figure 6. On-Resistance Variation with Junction Temperature

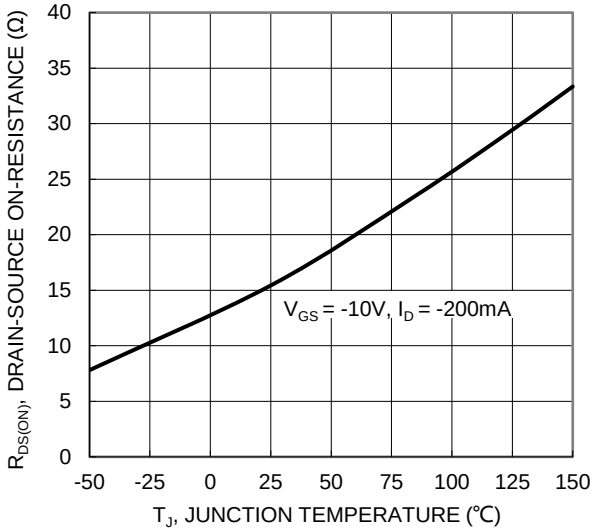


Figure 7. On-Resistance Variation with Junction Temperature

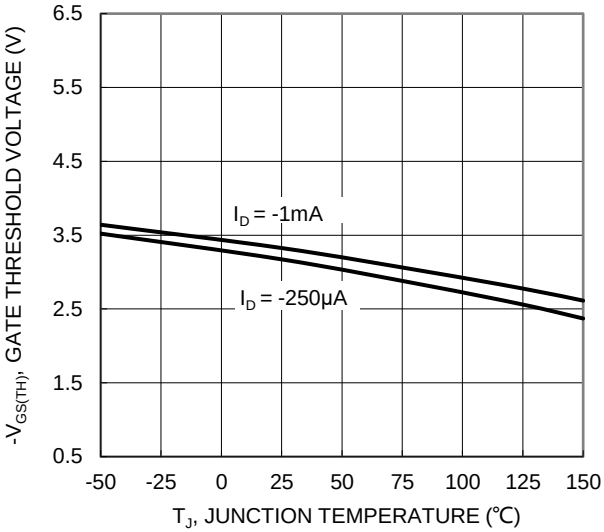


Figure 8. Gate Threshold Variation vs. Junction Temperature

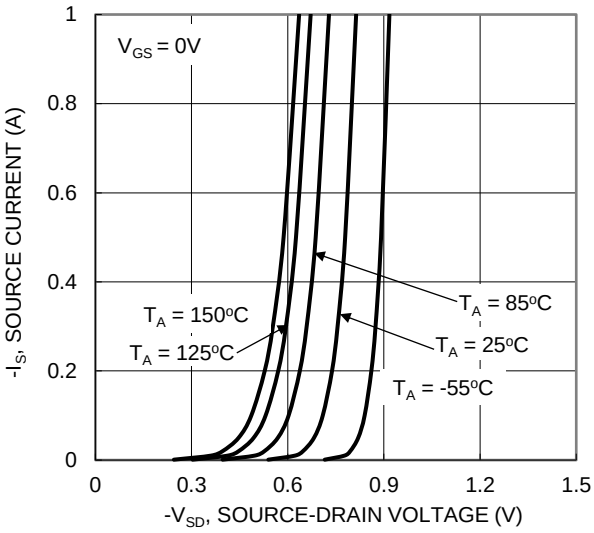


Figure 9. Diode Forward Voltage vs. Current

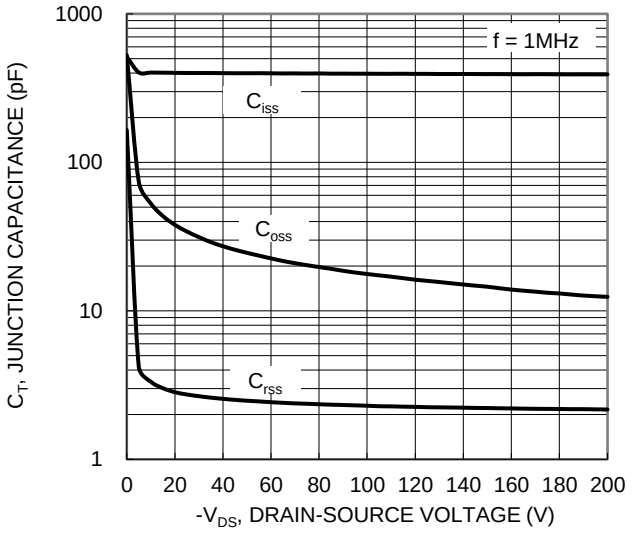


Figure 10. Typical Junction Capacitance

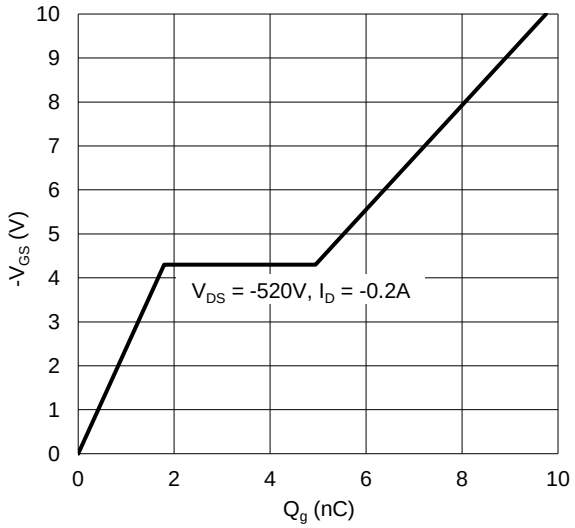


Figure 11. Gate Charge

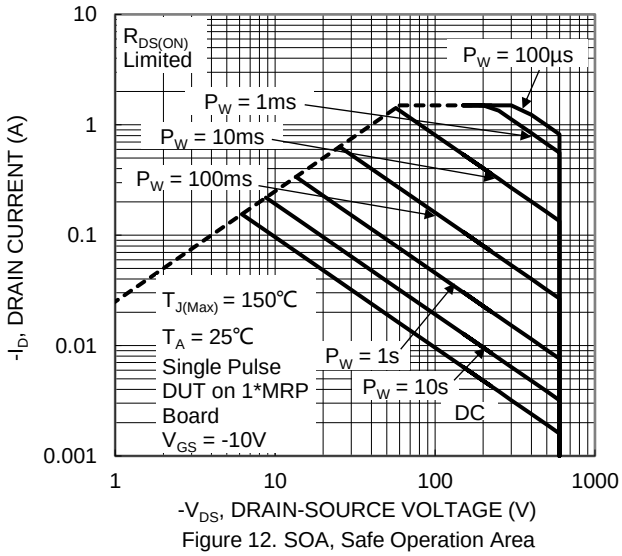


Figure 12. SOA, Safe Operation Area

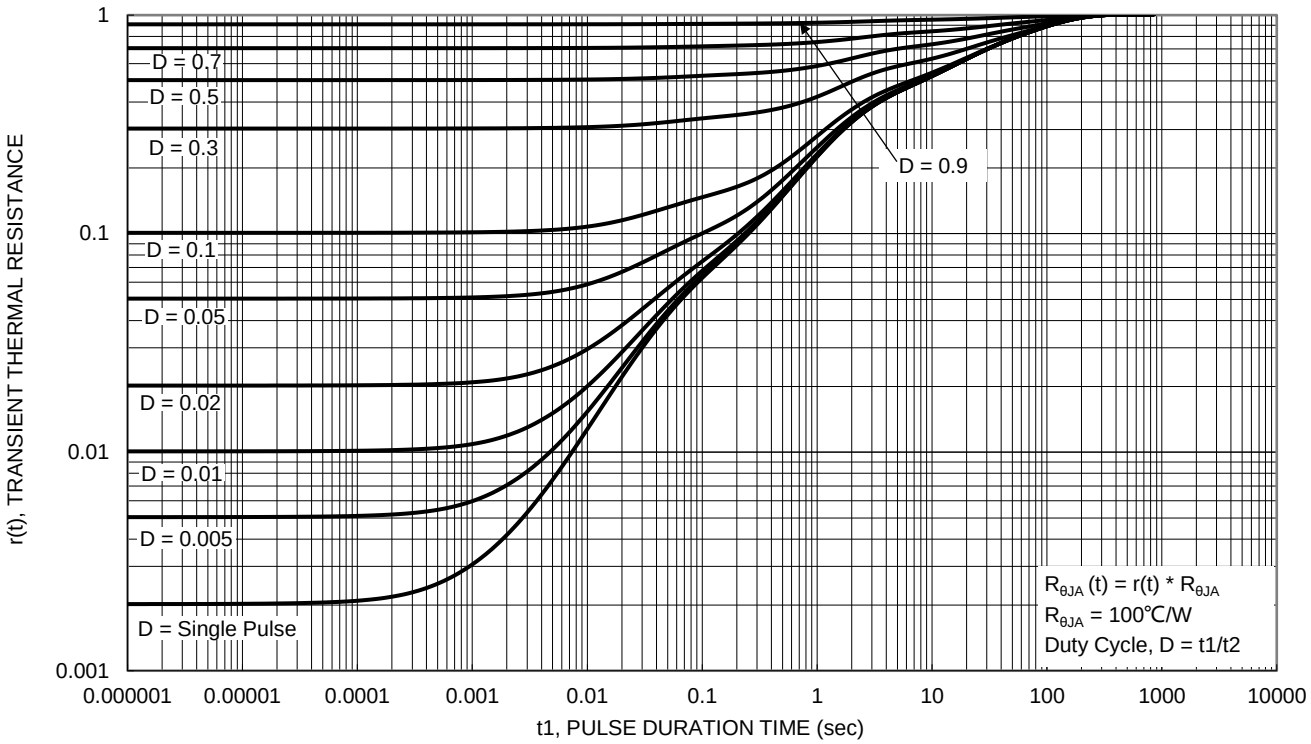
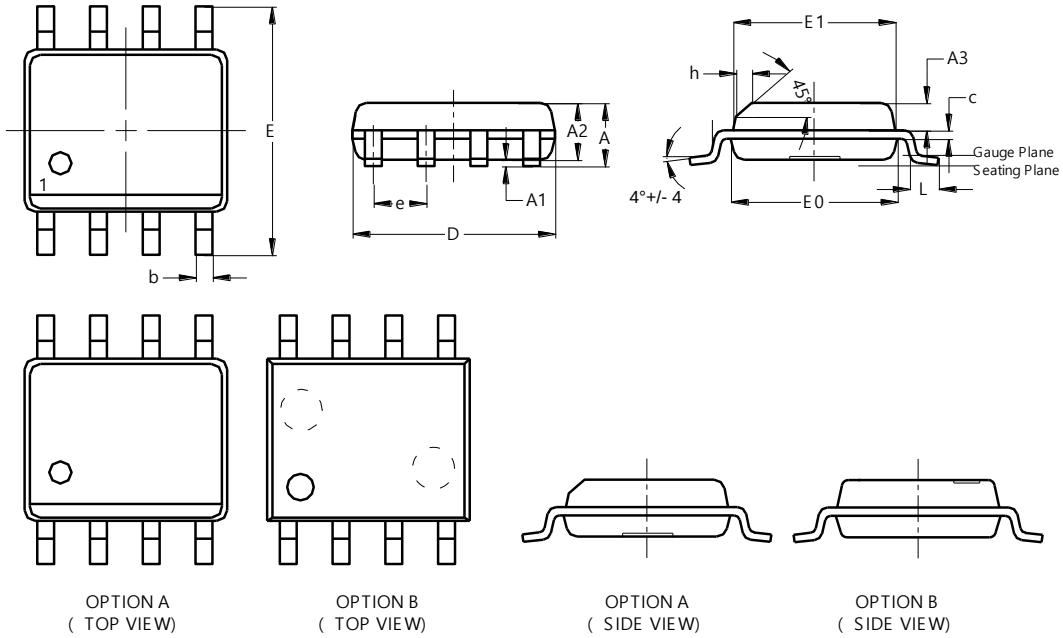


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8 (Standard B)

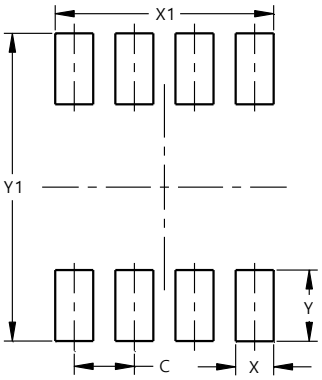


SO-8 (Standard B)			
Dim	**Min**	**Max**	**Typ**
A	1.35	1.75	1.45
A1	0.10	0.25	0.15
A3	0.60	0.70	0.65
b	0.30	0.51	0.40
c	0.15	0.25	0.20
D	4.70	5.10	4.90
E	5.80	6.20	6.00
E1	3.80	3.90	3.85
E0	3.80	4.00	3.90
e	--	--	1.27
h	--	--	0.35
L	0.40	1.27	--
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8 (Standard B)



Dimensions	Value (in mm)
C	1.27
X	0.802
X1	4.612
Y	1.505
Y1	6.50

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