



# Product Termination Notification



Product Group: Vishay Siliconix/Sep 22, 2014/PCN- SIL-0532014 Rev1

## End of Life Notification

**DESCRIPTION OF CHANGE:** The affected part numbers listed in this notification are not our focus products and are being discontinued. As replacements, we are recommending products from our SQ series of automotive qualified Mosfets that are manufactured using our preferred 45M cell automotive qualified process technology at Vishay's wafer Fab located at Fraunhoferstraße 1, 25524 Itzehoe, Germany (Vishay Siliconix Itzehoe GmbH or VSIG). VSIG has been an automotive Fab with ISO14001 and TS16949 certifications for more than 10 years.

The recommended replacement products will have slightly different electrical characteristics but have been identified as a suitable replacements for the existing products.

***Production of the affected parts from Santa Clara Fab will be terminated per the time schedule in this notification and last time buy orders must be received within the specified timeframe.***

**CLASSIFICATION OF CHANGE:** End of life

**REASON FOR CHANGE:** Closure of Fab at Santa Clara

**EXPECTED INFLUENCE ON QUALITY/RELIABILITY/PERFORMANCE:** Improvement

**PRODUCT CATAGORY:** Automotive MOSFETs

**VISHAY PART NUMBERS AFFECTED:** Affected and replacement part numbers are listed on the following page

**VISHAY BRAND(s):** Vishay-Siliconix

**QUALIFICATION DATA:** Replacement products are manufactured using 45M cell process technology which has been AEC Q101 qualified. Please refer to the subsequent pages to see summary of qualification report for the lead 45M product. Qualification report for individual part type will be provided in PPAP and upon request.

**SAMPLE AVAILABILITY:** Schedule of availability of qualified samples is listed on the following page. For samples, please email [automos.pcn@vishay.com](mailto:automos.pcn@vishay.com) with subject PCN-SIL-0532014 and include date by which samples are needed, required quantity, ship-to address and contact information including phone number.

**TIME SCHEDULE:** Last time buy and last time ship order dates are listed for each part number on the following page.

**ISSUED BY:** Shishir Rai, Product Marketing Manager (E-mail: [Shishir.Rai@Vishay.com](mailto:Shishir.Rai@Vishay.com))

**For further information, please contact your regional Vishay office.**

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Vishay Intertechnology, Inc.

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ONE OF THE WORLD'S LARGEST MANUFACTURERS OF DISCRETE SEMICONDUCTORS AND PASSIVE COMPONENT

Procedure #



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## VISHAY PART NUMBERS AFFECTED:

Affected Vishay Part Number	Replacement Part Number	Qualified Sample Availability Month	Last Time Buy Date	Last Time Ship Date
SQS466EEN-T1-GE3	SQS460EN-T1-GE3	Available	30-Mar-15	30-Sep-15
SY7844DP-T1-E3	SQJ844AEP-T1-GE3	Available	30-Mar-15	30-Sep-15
SY7850DP-T1-E3	SQJ850EP-T1-GE3	Available	30-Mar-15	30-Sep-15
SYD35N05-26L	SQD30N05-20L-GE3	Available	30-Mar-15	30-Sep-15
SYM47N10-24L-E3	SQM47N10-24L-GE3	Sep-14	30-Mar-15	30-Sep-15
SYD50N06-09L-E3	SQD50N06-09L-GE3	Oct-14	30-Apr-15	30-Oct-15
SYD25N15-52-E3	SQD25N15-52-GE3	Nov-14	30-May-15	30-Nov-15
SQC462AKGD	SQC462BKGD	Dec-14	30-Jun-15	30-Dec-15
SQM120N08-05-GE3	SQM120N10-09-GE3	Jan-15	30-Jun-15	30-Dec-15
SQJ962EP-T1-GE3	SQJ992EP-T1-GE3	Mar-15	30-Jun-15	30-Dec-15

## QUALIFICATION REPORT:

Qualification report for lead product SQM100N10-10-GE3 manufactured using 45M cell process technology (37M cell parts use same process as 45M cell technology) at VSIG Fab is provided in subsequent pages. Qualification report for the replacement parts listed above will be provided in PPAP and upon request.

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## Production Part Approval - Environmental Test Summary

Supplier:		Vishay Siliconix		General Specification:		AEC-Q101	
Supplier Part Number:		SQM100N10-10-GE3		Assembly Site:		Kaohsiung, Taiwan ROC	
Process Technology:		45M Cell N-Channel G2		Fab Site:		VSIG, Itzehoe Germany	
Item	Test	Test Conditions	# of Lots	S.S.	# Failed	Additional Requirements	Remarks
1	Pre- and Post Stress Electrical Test		*	All	0		
2	Pre-conditioning: Performed on surface mount devices (SMDs) prior to Temp Cycle, Autoclave, HAST, Power Cycle stresses only	J-STD-020C	*	All	0	@260 C	
3	External Visual: Inspect device construction, marking and workmanship. Electrical test not required.	Electricals per drawing	*	All	0		
4	Parametric Verification		3	30	0		Evaluation 1. 2. 3.
5	High Temperature Reverse Bias (HTRB): 1000 hours max rated junction temperature specified in the user/supplier specification with device reverse biased to 80% of maximum breakdown voltage specified or max junction temperature to avoid thermal runaway. TEST before, at 500 hours, and 1000 hours. JESD22 A108	175C 1000 HRS	3	77	0	DEVICE SPECIFIC:	Evaluation 1. 1380271 2. 1380272 3. 1380273
6	High Temperature Gate Bias (HTGB): 1000 hours at Ta = device maximum rated junction temperature with gate biased at 100% of maximum gate voltage rating indicated in the detail specification with device OFF. TEST before, at 500 hours, and 1000 hours. JESD22 A108	175C 1000 HRS	3	77	0	DEVICE SPECIFIC:	Evaluation 1. 1380271 2. 1380272 3. 1380273
7	Temperature Cycling: JESD22 A-104, Air to air. (See Reliability Product Data Summary):	1000CYC -65C ~ 150C	3	77	0	DEVICE SPECIFIC:	Evaluation 1. 1380271 2. 1380272 3. 1380273
8	Autoclave (Pressure Pot)	Ta = 121C, RH = 100%, 15psig, 96 hrs: Test before and after AC.	3	77	0	DEVICE SPECIFIC:	Evaluation 1. 1380271 2. 1380272 3. 1380273
9 alt	HAST	130C, 85% RH, 100 HRS	3	77	0	DEVICE SPECIFIC:	Evaluation 1. 1380271 2. 1380272 3. 1380273
10	Intermittent Operational Life (Power Cycle) Delta Tj = 100C	8572 CYC	3	77	0	DEVICE SPECIFIC:	Evaluation 1. 1380271 2. 1380272 3. 1380273



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Supplier:		Vishay Siliconix		General Specification:		AEC-Q101	
Supplier Part Number:		SQM100N10-10-GE3		Assembly Site:		Kaohsiung, Taiwan ROC	
Process Technology:		45M Cell N-Channel G2		Fab Site:		VSIG, Itzehoe Germany	
Item	Test	Test Conditions	# of Lots	S.S.	# Failed	Additional Requirements	Remarks
11	ESD Characterization - NOTE: Unless protected by internal ESD-specific protection circuitry, MOSFETs only have intrinsic protection that is dependent on the size of die and other environmental and physical factors, making them very sensitive to potential ESD damage and industry standard precautions should be taken not to expose them to any ESD. Due to the small size of MOSFET packages, these devices are generally not affected by the Charged Device Model, and we therefore substitute Machine Model testing.	Human Model	1	10	0	Passed 5.40KV AEC Q101	Evaluation 1.1340081
		Machine Model	1	10	0	Passed 1.10KV AEC Q101	
12	Destructive Physical Analysis	Cross-section / Cratering, CDF-AEC-Q101-004 Section 4	1	2x2	0		Evaluation 1.1340081
13	Physical Dimensions: Verify physical dimensions to the applicable user device packaging specification for dimensions and tolerances.	Siliconix Print Dimensions	N/A	N/A	N/A		See PPAP
14	Terminal Strength		N/A	N/A	N/A		SMD Device
15	Resistance to Solvent		N/A	N/A	N/A		Laser Marked
16	Constant Acceleration		N/A	N/A	N/A		SMD Device
17	Vibration Variable Frequency		N/A	N/A	N/A		SMD Device
18	Mechanical Shock		N/A	N/A	N/A		SMD Device
19	Hermiticity		N/A	N/A	N/A		SMD Device
20	Resistance to Solder Heat (Solder Dunk)	JESD22 B-106-A, 260C, 10sec. Test before and after RSH. SMD devices shall be fully submerged during test	3	55	0	GENERIC	Evaluation 1. 2. 3.
21	Solderability	Pb-Free - JESD201	3	15	0	GENERIC	Evaluation 1. 2. 3.
22	Thermal Resistance	JESD24-3	1	10	0	DEVICE SPECIFIC:	Evaluation 1. 1340479 2. 3.
23	Wire Bond Strength	MIL-STD-750 Method 2037	3	40	0	GENERIC	Evaluation 1. 2. 3.



## Production Part Approval - Environmental Test Summary

<b>Supplier:</b> Vishay Siliconix		<b>General Specification:</b> AEC-Q101	
<b>Supplier Part Number:</b> SQM100N10-10-GE3		<b>Assembly Site:</b> Kaohsiung, Taiwan ROC	
<b>Process Technology:</b> 45M Cell N-Channel G2		<b>Fab Site:</b> VSIG, Itzehoe Germany	

  

Item	Test	Test Conditions	# of Lots	S.S.	# Failed	Additional Requirements	Remarks
24	Bond Shear	AEC-Q101-003	3	40	0	GENERIC	Evaluation 1. 2. 3.
25	Die Shear	MIL-STD-750 Method 2017	3	10	0	GENERIC	Evaluation 1. 2. 3.
26	UIS Testing	Non-destructive mode	100%	100%	0		100% tested at Final Test
27	Dielectric Integrity	Non-destructive mode	100%	100%	0		100% tested at Final Test

Note: \* = Samples taken from many lots

Prepared by: Julian Chen Reliability Engineer	5/16/2014
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Approved by: Arthur Chiang Director of Reliability	5/16/2014
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